

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/701,301
Filing Date November 3, 2003
Inventor John T. Moore
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Ghyka, A.
Attorney's Docket No. MI22-2438
Title: Methods of Forming Dielectric Materials and Methods of Processing Semiconductor Substrates

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Dated: May 14, 2004

Respectfully submitted,

By: Jennifer J Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
M122-2438

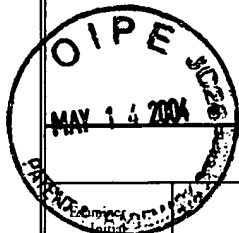
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LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

APPLICANT
John T. Moore

FILING DATE
November 3, 2003

GROUP
2815



U.S. PATENT DOCUMENTS

	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA	2003/0034518 A1	02-2003	YoshiKawa			May 17, 2001
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM							
AN							
AO							
AP							
AQ							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

AR			
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EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.